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Attorney's Docket No.: S1022/8718

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: S1022/8718
Serial No.: Unassigned
Filed: Herewith
For: CARRY SAVE ADDERS

Examiner: Unassigned
Art Unit: Unassigned

Box Patent Application
Commissioner for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir/Madam:

Prior to examination please amend the above-identified application as follows:

IN THE SPECIFICATION

Please insert the following paragraphs on page 15, after line 22.

--Having thus described at least one illustrative embodiment of the invention, various alterations, modifications, and improvements will readily occur to those skilled in the art. Such alterations, modifications, and improvements are intended to be within the spirit and scope of the invention. Accordingly, the foregoing description is by way of example only and is not intended as limiting. The invention is limited only as defined in the following claims and the equivalents thereto.

What is claimed is:--

IN THE ABSTRACT

Please amend the abstract as follows.

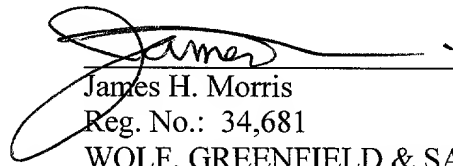
Applicant presents the abstract as amended below and encloses a separate sheet indicating the amendments to the abstract with bracketing and underlining.

A carry save adder circuit for reducing the number of inputs to a lower number of outputs, the carry save adder circuit including four carry save adders, the four carry save adders being arranged in two layers with the first and second carry save adders being arranged in a first of said layers and the third and fourth carry save adders being arranged in a second of the layers, said third and fourth carry save adders being arranged to provide the outputs, the third and fourth carry save adders each receiving at least one output from each of the first and second carry save adders and the first and second carry save adders being arranged to receive at least some of the inputs.

REMARKS

This is a Preliminary Amendment in which Applicant has amended the abstract and the specification. No new matter has been added. An early and favorable action is respectfully requested.

Respectfully submitted,


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AMENDED ABSTRACT SHOWING THE AMENDMENTS

A carry save adder circuit for reducing the number of inputs to a lower number of outputs,
5 [said] the carry save adder circuit [comprising] including four carry save adders, [said] the
four carry save adders being arranged in two layers with the first and second carry save
adders being arranged in a first of said layers and the third and fourth carry save adders
being arranged in a second of [said] the layers, said third and fourth carry save adders being
arranged to provide [said] the outputs, [said] the third and fourth carry save adders each
10 receiving at least one output from each of [said] the first and second carry save adders and
the first and second carry save adders being arranged to receive at least some of [said] the
inputs.